



# Electronic Filing System (EFS) Data

## Electronic Patent Application Submission

### USPTO Use Only

EFS ID: 56632

Application ID: 10619638



Title of Invention: Content Addressable Memory  
(CAM) Devices Having Error  
Detection and Correction Control  
Circuits Therein and Methods of  
Operating Same

First Named Inventor: Chuen-Der Lien

Domestic/Foreign Application: Domestic Application

Filing Date: 2003-07-15

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Submission Type: Information Disclosure  
Statement

Filing Type:

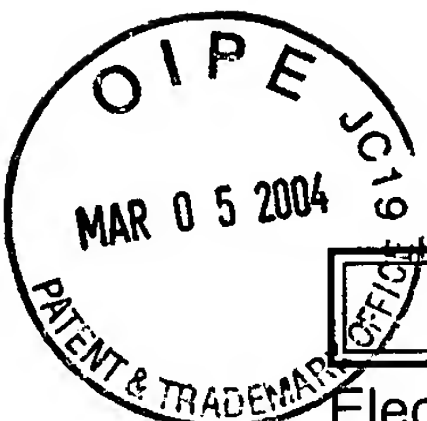
Confirmation number: 1228

Attorney Docket Number: 5646-91IP

Total Fees Authorized:


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Office,ou=Department of Commerce,o=U.S. Government,c=US

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Electronic Version v1.1  
Stylesheet Version v1.1.0


Title of Invention	Content Addressable Memory (CAM) Devices Having Error Detection and Correction Control Circuits Therein and Methods of Operating Same							
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Documents being submitted us-ids		Files 5646-91IP-usidst.xml us-ids.dtd us-ids.xsl						
Comments								



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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<table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6700810</td><td>2004-03-02</td><td>Ichiriu et al.</td><td>B1</td><td>365</td><td>49</td></tr><tr><td></td><td>2</td><td>6690595</td><td>2004-02-10</td><td>Srinivasan et al.</td><td>B1</td><td>365</td><td>49</td></tr><tr><td></td><td>3</td><td>6618281</td><td>2003-09-09</td><td>Gordon</td><td>B1</td><td>365</td><td>49</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6700810	2004-03-02	Ichiriu et al.	B1	365	49		2	6690595	2004-02-10	Srinivasan et al.	B1	365	49		3	6618281	2003-09-09	Gordon	B1	365	49
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